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1. This document has been translated by computer. So the translation may not reflect the original precisely.
2. \*\*\*\* shows the word which can not be translated.
3. In the drawings, any words are not translated.

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**DESCRIPTION OF DRAWINGS**

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[Brief Description of the Drawings]

[Drawing 1] It is the schematic diagram showing an example of this invention.

[Drawing 2] It is a wave form chart for explaining this invention.

[Drawing 3] It is the schematic diagram showing other examples of this invention.

[Drawing 4] It is the schematic diagram showing other examples of this invention.

[Drawing 5] It is drawing for explaining a TDMA system.

[Drawing 6] It is a schematic diagram for explaining this invention.

[Description of Notations]

10 Sending Circuit

13 Quadrature Modulation Circuit

14 Gain Control Circuit

15 Power Amplification

20 System Controller

30 APC Circuit

33 Electrical-Potential-Difference Comparator Circuit

41 Envelope Detector

42 Subtractor Circuit

43 Sampling Hold Circuit

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[Translation done.]

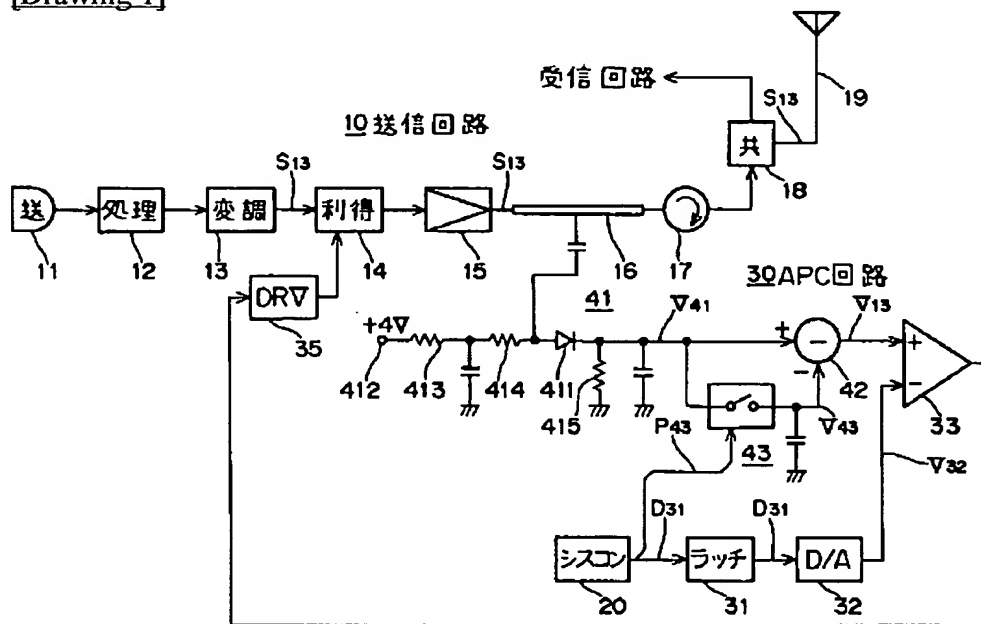
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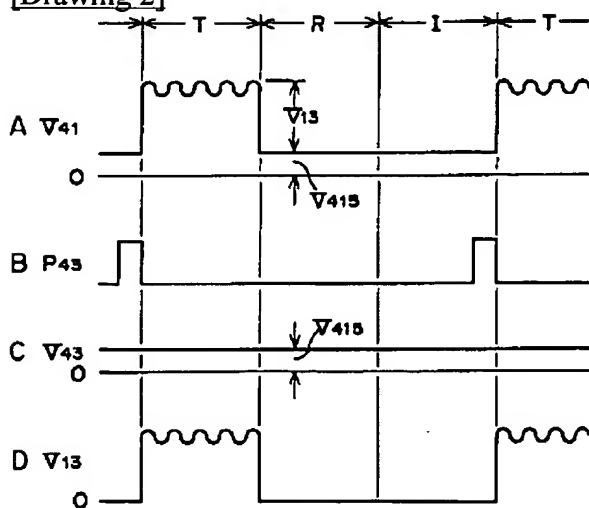
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DRAWINGS

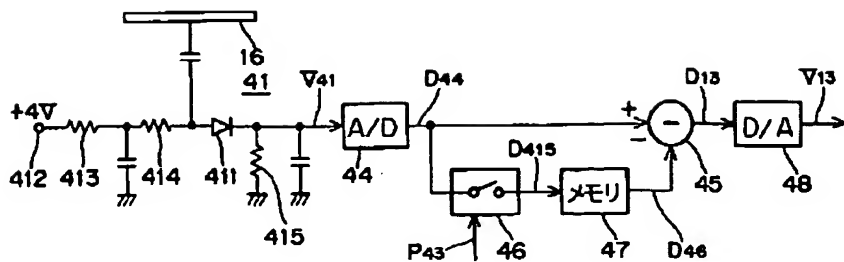
[Drawing 1]



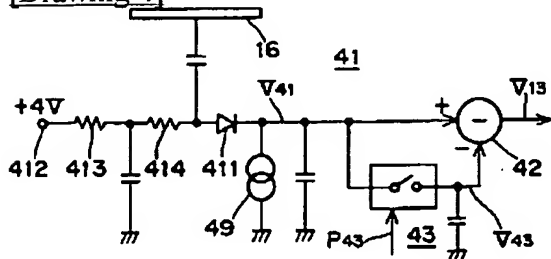
[Drawing 2]



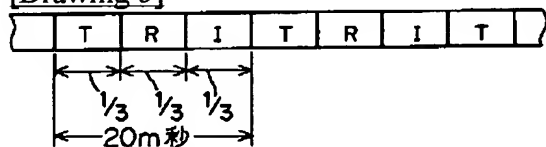
[Drawing 3]



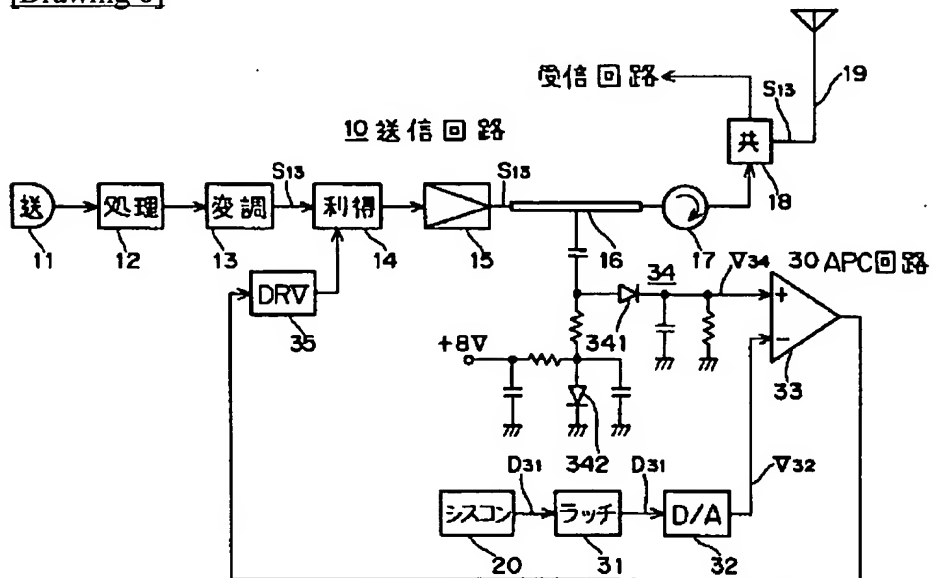
[Drawing 4]



[Drawing 5]



[Drawing 6]



[Translation done.]